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Lackey, D.E.; Zuchowski, P.S.; Bednar, T.R.; Stout, D.W.; Gould, S.W.; Cohn, Computer Aided Design, 2002. ICCAD 2002. IEEE/ACM International Conference on  
10-14 Nov. 2002 Page(s):195 - 202  
Digital Object Identifier 10.1109/ICCAD.2002.1167534  
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- ☐ 2. **A semi-custom voltage-island technique and its application to high-speed [CMOS active power reduction]**  
Carballo, J.-A.; Burns, J.L.; Seung-Moon Yoo; Vo, I.; Norman, V.R.; Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003 Symposium on  
25-27 Aug. 2003 Page(s):60 - 65  
Digital Object Identifier 10.1109/LPE.2003.1231836  
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- ☐ 3. **New trends in low power SoC design technologies**  
Wei Hwang; SOC Conference, 2003. Proceedings. IEEE International [Systems-on-Chip]  
17-20 Sept. 2003 Page(s):422  
Digital Object Identifier 10.1109/SOC.2003.1241559  
[AbstractPlus](#) | Full Text: [PDF\(185 KB\)](#) IEEE CNF  
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- ☐ 4. **FPGA-based islanding detection for grid connected inverter**  
Tunlasakun, K.; Kirtikara, K.; Thepa, S.; Monyakul, V.; Industrial Electronics Society, 2004. IECON 2004. 30th Annual Conference of the  
Volume 2, 2-6 Nov. 2004 Page(s):1978 - 1982 Vol. 2  
Digital Object Identifier 10.1109/IECON.2004.1431888  
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L2	33968	(voltage or power or vss or vdd or vcc or v0 or v1 or v2) near5 (nest\$3 or within or inside) same (ring or island or area or region or terrain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 18:45
L3	1438	(voltage or power or vss or vdd or vcc or v0 or v1 or v2) near5 (nest\$3 or within or inside) same (ring or island or area or region or terrain) and ("716"/\$.ccls. or "326"/\$.ccls. or "365"/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 18:46
L4	1207	(voltage or power or vss or vdd or vcc or v0 or v1 or v2) near5 (nest\$3 or within or inside) same (ring or island or area or region or terrain) and ("716"/\$.ccls. or "365"/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 18:47
L5	228	(voltage or power or vss or vdd or vcc or v0 or v1 or v2) near5 (nest\$3 or within or inside) same (ring or island or area or region or terrain) and ("716"/\$.ccls. or "365"/\$.ccls.) and (bock or net)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 18:47
S19	2572	(power near4 supply) same (voltage near4 supply) and island	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S20	450	((power near4 supply) same (voltage near4 supply)) and island and network and input and output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S21	22	((power near4 supply) same (voltage near4 supply)) and island and network and input and output and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13

S22	39	((power near4 supply) same (voltage near4 supply)) and island and network and input and output and terrain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:03
S23	47	((power near4 supply) same (voltage near4 supply)) and island and network and input and output and vdd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S24	29	((power near4 supply) same (voltage near4 supply)) and island and network and input and output and vdd and distribution	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S25	66323	voltage same net\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S26	172	voltage same net\$3 same island	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S27	120	voltage same net\$3 same v1 same v2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S28	156	(voltage near5 net\$3) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13
S29	46	(voltage near5 net\$3) and vdd and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 14:13

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L1	7	"716"/\$.ccls. and (voltage near2 (conver\$4 shift\$4)) and (voltage suppl\$4) near3 (isolat\$4 partition) and @ad<"20030708"	US-PGPUB; USPAT	OR	ON	2006/02/03 14:34
L2	196	"716"/\$.ccls. and (voltage same (conver\$4 shift\$4) cell ) and (voltage suppl\$4) same (isolat\$4 partition\$4 same cell ) and @ad<"20030708"	US-PGPUB; USPAT	OR	ON	2006/02/03 14:35
L4	11	"716"/\$.ccls. and (voltage same (conver\$4 shift\$4) same cell ) and (voltage suppl\$4) same (isolat\$4 partition\$4) same cell and @ad<"20030708"	US-PGPUB; USPAT	OR	ON	2006/02/03 14:40
L5	10	"716"/\$.ccls. and (voltage same (conver\$4 shift\$4) same cell ) and (voltage suppl\$4) same (allocat\$4) same cell and @ad<"20030708"	US-PGPUB; USPAT	OR	ON	2006/02/03 14:41
L6	0	"716"/\$.ccls. and (voltage same (conver\$4 shift\$4) same cell ) and (voltage suppl\$4) same (allocat\$4) same cell and @rlad<"20030708"	US-PGPUB; USPAT	OR	ON	2006/02/03 14:40
L7	0	"716"/\$.ccls. and (voltage same (conver\$4 shift\$4) same cell ) and (voltage suppl\$4) same (allocat\$4) same cell and @rlad<"20030708"	US-PGPUB; USPAT	OR	ON	2006/02/03 14:41